

CLAIMS:

1. A plurality of integrated circuits, each of the integrated circuits comprising a processor and non-volatile memory, and including code for running identical software processes, wherein each of the integrated
5 circuits also includes secret information used by the software process, the secret information in each chip being located in a different location in the memory relative to a plurality of the other chips.
2. A plurality of integrated circuits according to claim 1, wherein the code on each integrated circuit is such that the software process of each chip knows the location in memory via which the secret information is
10 accessible.
3. A method of manufacturing a plurality of the integrated circuits of claim 2, including the steps of:
manufacturing a plurality of physical integrated circuits; and
injecting, into the non-volatile memory of each of the integrated circuits:
15 code for running a software process; and
secret information;
wherein the secret information is positioned in relatively different locations of the non-volatile
memories and the code on each integrated circuit is such that the software process of each integrated circuit
knows the location in memory via which the secret information is accessible on that integrated circuit.
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